

Notice of References Cited	Application/Control No. 09/487,483	Applicant(s)/Patent Under Reexamination SHIBA ET AL.	
	Examiner Michael J. Simitoski	Art Unit 2134	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,037,093	07-1977	Gregg et al.	708/492
	B	US-5,379,243 A	01-1995	Greenberger et al.	708/492
	C	US-6,049,815 A	04-2000	Lambert et al.	708/492
	D	US-6,178,436 B1	01-2001	Blake et al.	708/492
	E	US-2002/0138534 A1	09-2002	Drescher, Wolfram	708/492
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Drescher, Wolfram et al. "VLSI Architecture for Datapath Integration of Arithmetic Over GF(2 ^m) on Digital Signal Processors", 1997 IEEE.
	V	Paar, Christof et al. "Efficient Multiplier Architectures for Galois Fields GF(2 ⁴ⁿ)", 1998 IEEE.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.